

SPANSION™ FLASH MEMORY DESIGN TOOLS

Innovative Tools and Resources to
Reduce Time-to-Market

Design Faster with Spansion™ Tools

Spansion is dedicated to providing tools and subsystems that help customers easily and quickly design Spansion Flash memory into embedded and wireless products. Several of these resources are uniquely available from Spansion – one more reason to make Spansion your first choice for Flash memory solutions.

CAD Modeling Software

Spansion's website offers easy access to a wide range of complimentary simulation models based on popular industry standards.

VHDL and Verilog Models

Spansion has developed a comprehensive set of industry-standard VHDL and Verilog behavioral simulation models for our Flash memory products. Spansion's complimentary VHDL and Verilog simulation models are designed to be compatible with virtually any EDA modeling environment.

IBIS Models

IBIS (I/O Buffer Information Specification) is a behavioral description of I/O buffers and package characteristics. A consortium of companies developed the IBIS model format for the express purpose of giving customers the data they need without sharing proprietary H-SPICE models. Use Spansion's complimentary IBIS models to verify signal integrity, EMI, and transition response.

OrCAD® Schematic Symbols

To increase your design quality while saving time and costs, Spansion engineers have developed a complete library of OrCAD® schematic symbols for Spansion™ Flash memory devices. These predefined symbols can be downloaded to printed circuit board design applications, enhancing the productivity of PCB designers. The OrCAD symbol library is available on Spansion's website.



PISMO

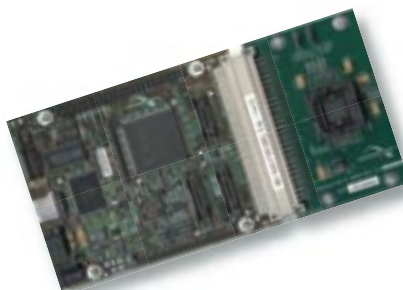
PISMO (Platform Independent Storage Module) provides an industry-standard memory expansion interface for development platforms. PISMO enables streamlined memory validation on development boards and is central to many Spansion™ design tools. Spansion offers comprehensive support of PISMO modules containing Spansion Flash memory.

Spansion™ Logic Analyzer Interface Module

Designed to simplify debugging, PISMO modules can be connected to a companion logic analyzer module that provides logic analyzer access to critical control, address, and data signals. The module simplifies the design of evaluation and prototype systems without the added complexity and cost of adding logic-analyzer headers on every board.

Spansion™ USB Programmer

The Spansion USB Programmer (SUP) is a cost-effective single-device Flash memory programmer. The SUP can program and verify Spansion Flash memory using the USB port of a PC running the Microsoft® Windows® operating system.



Spansion™ PCI Card

The Spansion PCI card connects PISMO interface-compliant memory modules to PCI-based hosts. The Spansion PCI card provides a complete hardware platform for developing drivers and utility applications using popular PC-based development systems.



Application Notes

Spansion's offers an ever expanding library of technical documentation, including white papers, technology bulletins, datasheets, design guides, and technical manuals. Available on the Spansion website (www.spansion.com), these application notes cover a variety of technical topics—everything from designing scalable wireless platforms, to maximizing factory programming throughput using accelerated Flash operations, to interfacing a microcontroller to a specific Flash memory part.

Download or Order Today

Spansion hardware tools, simulation models, OrCAD symbols, and application notes are available from your Spansion sales representative or directly from Spansion's website, www.spansion.com.

For More Information

Comprehensive information about Spansion™ Flash memory design tools is available at <http://www.spansion.com>.

Spansion products are available through AMD or Fujitsu. To learn how Spansion can help you bring products to market faster, please visit www.spansion.com or contact your local AMD or Fujitsu Distributor.

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