

X-ray Inspection Considerations for Surface-Mounted Flash ICs

by David Lehtonen

Application Note



This document is intended to help those customers who perform X-ray inspection of the surface-mounted integrated circuits (ICs) on their circuit boards. X-rays behave basically the same as visible light rays, since both are wavelike forms of electromagnetic energy carried by particles called photons. The difference between X-rays and visible light rays is only the energy level of the individual photons, which is also expressed through the wavelength of the rays. Just as filtering of visible light wavelengths (that is, energies) can be used effectively to prevent damage to photosensitive materials, Spansion has shown that filtering of specific X-ray energy levels can be used to prevent damage to X-ray sensitive semiconductor ICs.

It has been well established that semiconductor ICs can suffer irreversible damage from charging effects caused by X-ray energy. While this phenomenon does not always result in a hard failure, customers often have no way to recover from the effects of the X-ray exposure. The following table shows the approximate total X-ray dose damage of commercial off-the-shelf (COTS) devices:

Type of Semiconductor Device (COTS)	Total Dose Threshold (K Rads)
Linear	2-50
Mixed Signal	2-30
Flash Memory	5-15
DRAM	15-50
Microprocessors	15-70

Spansion studies have also shown that there is a substantial X-ray dose variation among inspection equipment suppliers as shown in the table below:

Supplier	Approx. Dose (Rads)
A	0.057
B	3
C	10
D	12
E	25
F	35
G	60
H	700

We found that in most cases, these suppliers have recommended X-ray doses that are significantly higher than what is necessary to achieve successful inspection results. The key is to minimize the total cumulative dose to the IC while achieving a useful inspection image.

The original goal of Spansion's experimentation was to detect 50 μm copper traces (typical for a PWB) and the underlying 0.5 mm IC solder balls at the lowest possible X-ray dose. We quickly proved that the X-ray tube voltage is not the predominant factor for damaging charge storage cells. After discussing our results with several suppliers, it became apparent that the choice of X-ray tube filtering is the key factor of concern.

It was concluded that silicon dose is sensitive ONLY to X-rays with energy in the range 2-9 KeV; that 50 μm Cu traces are best imaged with X-ray energy of 9-20 KeV; and that tin and lead are well-imaged by X-rays

over the energy range of 30-50 KeV and higher. While many thick metal filters effectively reduce Si dose, they also have the effect of making the relatively thin copper traces in a PWB very difficult to image by strongly absorbing X-rays in the energy range 9-20 KeV. A thin 300 μm zinc filter will be a very effective agent to absorb very soft X-rays to which silicon is vulnerable, yet transmit soft and medium energy X-rays required to obtain good radiographs of thin copper traces and solder balls. Zinc foil can be integrated with the inspection "carrier" or put near the X-ray source.

As a general rule, if customers have no filtering capability, they should limit the **cumulative** X-ray inspection exposure to the SMT memory devices to 1,000 Rads or less. Spansion has submitted a patent for the use of zinc filtering that will enable X-ray suppliers to produce systems that are better 'tuned' for the electronics industry.

Revision History

Section	Description
Revision 01 (May 21, 2007)	
	Initial release.

Colophon

The products described in this document are designed, developed and manufactured as contemplated for general use, including without limitation, ordinary industrial use, general office use, personal use, and household use, but are not designed, developed and manufactured as contemplated (1) for any use that includes fatal risks or dangers that, unless extremely high safety is secured, could have a serious effect to the public, and could lead directly to death, personal injury, severe physical damage or other loss (i.e., nuclear reaction control in nuclear facility, aircraft flight control, air traffic control, mass transport control, medical life support system, missile launch control in weapon system), or (2) for any use where chance of failure is intolerable (i.e., submersible repeater and artificial satellite). Please note that Spansion will not be liable to you and/or any third party for any claims or damages arising in connection with above-mentioned uses of the products. Any semiconductor devices have an inherent chance of failure. You must protect against injury, damage or loss from such failures by incorporating safety design measures into your facility and equipment such as redundancy, fire protection, and prevention of over-current levels and other abnormal operating conditions. If any products described in this document represent goods or technologies subject to certain restrictions on export under the Foreign Exchange and Foreign Trade Law of Japan, the US Export Administration Regulations or the applicable laws of any other country, the prior authorization by the respective government entity will be required for export of those products.

Trademarks and Notice

The contents of this document are subject to change without notice. This document may contain information on a Spansion product under development by Spansion. Spansion reserves the right to change or discontinue work on any product without notice. The information in this document is provided as is without warranty or guarantee of any kind as to its accuracy, completeness, operability, fitness for particular purpose, merchantability, non-infringement of third-party rights, or any other warranty, express, implied, or statutory. Spansion assumes no liability for any damages of any kind arising out of the use of the information in this document.

Copyright © 2007 Spansion Inc. All rights reserved. Spansion®, the Spansion Logo, MirrorBit®, MirrorBit® Eclipse™, ORNAND™, HD-SIM™ and combinations thereof, are trademarks of Spansion LLC in the US and other countries. Other names used are for informational purposes only and may be trademarks of their respective owners.